

4th International Workshop on Open Source Supercomputing (OpenSuCo 4)

Agenda for Monday, 2018 November 12
Held In Conjunction with SC18

09:00 – 09:20 *Welcome | OpenSuCo Organizers*

09:20 – 09:40 Performance and Communication Modeling for Exascale Proxy Architecture in Aspen | Mariam Umar (Intel)

09:40 – 10:00 FireSim: FPGA-Accelerated Cycle-Exact Scale-Out System Simulation in the Public Cloud | Sagar Karandikar (UC Berkeley)

10:00 – 10:30 *Workshop Morning Break*

10:30 – 10:50 D-SAGA: A Domain-Specific Architecture for Fast Processing of Large Graph Applications | Rashmi Agrawal & Donato Kava (Boston University)

10:50 – 11:10 How System-Level Design Can Benefit the Progress of Open-Source Hardware | Luca Carloni (Columbia University)

11:10 – 12:10 **Keynote Speaker:** Full Stack Open Source Supercomputing | Nicholas Malaya (AMD)

12:10 – 12:30 **Panel:** Open-Source Hardware | Sagar Karandikar (UC Berkeley), Donato Kava (Boston University), & Luca Carloni (Columbia University)

12:30 – 14:00 *Lunch*

14:00 – 14:20 HPC PowerStack: a community-wide open collaboration for enabling system-wide power efficiency | Siddhartha Jana (Intel)

14:20 – 14:40 Open-Source Supercomputing | John Gustafson

14:40 – 15:00 GraphBLAS Forum and Its Relevant Software Zoo | Aydin Buluç (LBNL)

15:00 – 15:30 *Workshop Afternoon Break*

15:30 – 15:50 Driving Asynchronous Distributed Tasks with Events | Nick Brown (University of Edinburgh)

15:50 – 16:10 Opportunities for Open-Source Development for D-Wave Systems | Steve Reinhardt (D-Wave Systems)

16:10 – 16:30 Open-Source Modeling and Simulation | Gwendolyn Renae Voskuilen (SNL)

16:30 – 16:50 **Panel:** Open-Source Software | Nick Brown (University of Edinburgh), Steve Reinhardt (D-Wave Systems), & Gwendolyn Renae Voskuilen (SNL)

16:50 – 17:00 *Closing Remarks | OpenSuCo Organizers*